$\qquad$ Section

## 1) Logic Gates ( 20 points)

You should recognize the digital gates in the following circuit as the ones introduced in the lab and/or in class.

a) What kind of gate is U2A? (2 points)

Answer: U2A is a three input NOR gate
b) What is the truth table for gate U2A? (6 points)

Answer:

| $\boldsymbol{A}$ | $\boldsymbol{B}$ | $\boldsymbol{C}$ | $\boldsymbol{Y}$ |
| :--- | :--- | :--- | :--- |
| 0 | 0 | 0 | 1 |
| 0 | 0 | 1 | 0 |
| 0 | 1 | 0 | 0 |
| 0 | 1 | 1 | 0 |
| 1 | 0 | 0 | 0 |
| 1 | 0 | 1 | 0 |
| 1 | 1 | 0 | 0 |
| 1 | 1 | 1 | 0 |

c) Indicate which of the three plots on the following page represents the output of the circuit. Show any work below for partial credit. (12 points)
work:

| DSTM1 | U2A:Y | U5A:Y | U3A: $\boldsymbol{Y}$ | $\boldsymbol{U 1 A}: \boldsymbol{Y}$ |
| :--- | :--- | :--- | :--- | :--- |
| 0 | 1 | 1 | 1 | 1 |
| 1 | 0 | 1 | 0 | 0 |

Note: U1A:B is the same as U3A:Y
$\qquad$

Circuits for question 1 part c).
A.

B. Answer: This one is CORRECT.

C.

$\qquad$
2) NAND Gate Circuits ( $\mathbf{2 0}$ points)

It is possible to configure all standard gates using just NAND gates. The figure below show one such combination of NANDS.

a. On the following plot, identify which signal goes with which location (6 points).


Answer: order from top to bottom: $F, A, B, C, D, E$
b. Draw a truth table for the circuit, showing the inputs, the output and as many steps in between as you need to determine how it works (10 points).

Answer:

| $\boldsymbol{A}$ | $\boldsymbol{B}$ | $\boldsymbol{C}$ | $\boldsymbol{D}$ | $\boldsymbol{E}$ | $\boldsymbol{F}$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 0 | 0 | 1 | 1 | 0 | 1 |
| 0 | 1 | 1 | 0 | 1 | 0 |
| 1 | 0 | 0 | 1 | 1 | 0 |
| 1 | 1 | 0 | 0 | 1 | 0 |

c. Which single type of gate is this circuit equivalent to (4 points)?

Answer: two input NOR gate
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## 3) Combinational Logic ( 20 points)



Draw the truth table for the circuit above:
Answer:

| A | B | C | D | E | F | G | H | I | Q |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 0 | 0 | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 1 |
| 0 | 1 | 0 | 1 | 1 | 0 | 1 | 1 | 0 | 1 |
| 1 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 0 | 1 |
| 1 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 1 |

$\qquad$

## 4) Digital-to-Analog Converter ( 20 points)

a) In figure below, $\mathrm{R} 5=20 \mathrm{~K}$ ? , $\mathrm{R} 6=1 \mathrm{~K}$ ? and $\mathrm{R} 7=5 \mathrm{~K}$ ? . This configuration of opamps and resistors can produce an analog output voltage equal to the binary word ABCD input at the left. Assume that you are working with TTL devices, so the voltage levels for ones and zeros are TTL levels ( 0 V and 5 V ). Select values for R1, R2, R3, and R4 so that the output voltage will be the decimal equivalent of ABCD. For example if $\mathrm{ABCD}=1010$, or equivalently $\mathrm{VA}=\mathrm{VC}=5 \mathrm{v}, \mathrm{VB}=\mathrm{VD}=0 \mathrm{v}$, Vout $=10 \mathrm{v}$. The circuit should work for all possible ABCD combinations. (12 points)
$R 1=$
$\mathrm{R} 2=$
$\mathrm{R} 3=$
$\mathrm{R} 4=$
b) Your choices of resistors should work for any number, but specifically show that your values work for the two binary numbers $\mathrm{ABCD}=0010$ and $\mathrm{ABCD}=1011$. ( 8 points)

a) Answer:

Vout $=-R 5 x[V A / R 1+V B / R 2+V C / R 3+V D / R 4] x-(R 7 / R 6)$
Vout $=[V A / R 1+V B / R 2+V C / R 3+V D / R 4] \times[(R 7 x R 5) / R 6]$
$(R 7 x R 5) / R 6=(5 K x 20 K) / 1 K=100 K$

| input | bit | Vout | $V A$ | $V B$ | $V C$ | $V D$ | Equation | $R$ |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 0001 | 1 | $1 V$ | 0 | 0 | 0 | $5 V$ | Vout $=100$ KxVD/R4 | $R 4=500 \mathrm{~K}$ |
| 0010 | 2 | $2 V$ | 0 | 0 | $5 V$ | 0 | Vout $=100$ KxVC/R3 | $R 3=250 \mathrm{~K}$ |
| 0100 | 3 | $4 V$ | 0 | $5 V$ | 0 | 0 | Vout $=100 \mathrm{KxVB/R} 2$ | $R 2=125 \mathrm{~K}$ |
| 1000 | 3 | $8 V$ | $5 V$ | 0 | 0 | 0 | Vout $=100$ KxVA/R1 | $R 1=62.5 \mathrm{~K}$ |

Therefore, $R 1=62.5 \mathrm{~K}$ ohms, $R 2=125 \mathrm{~K}$ ohms, $R 3=250 \mathrm{~K}$ ohms, $R 4=500 \mathrm{~K}$ ohms.
(Show your work here for question 4.)
b) Answer:

$$
\begin{aligned}
& A B C D=0010(\text { binary })=2(\text { decimal }) \\
& \text { Vout }= 2 V=?[V A / R 1+V B / R 2+V C / R 3+V D / R 4] *(R 6 * R 5) / R 7 \\
& 2 V=?[5 / 250 K] * 100 K=2 V(\text { checks })
\end{aligned}
$$

$$
A B C D=1101(\text { binary })=11(\text { decimal })
$$

$$
\text { Vout }=11 V=?[V A / R 1+V B / R 2+V C / R 3+V D / R 4] *(R 6 * R 5) / R 7
$$

$$
11 V=?[5 / 62.5 K+5 / 250 K+5 / 500 K] * 100 K
$$

$$
11 \mathrm{~V}=?[.08+.02+.01] * 100=11 \mathrm{~V}(\text { checks })
$$

5) Transistor Circuit (20 points)

a) Redraw the circuit when the diode in the transistor is "off". Show the transistor as a switch in the appropriate position. What is the value of the voltage at point C ? ( 6 points)


$$
V C=(R 3) /(R 2+R 3) V 2=(1 K)(1 K+1 K) 9=4.5 V
$$

$\qquad$
b) Redraw the circuit when the diode in the transistor is "on". Show the transistor as a switch in the appropriate position. What is the value of the voltage at point C ? ( 6 points)

Answer:

$V C=0 V$ (attached to ground)
c) Indicate which of the three plots on the following page corresponds to the circuit above AND indicate on the chosen plot, which signal corresponds to which of the above points (A, B, C, D) on the circuit. (8 points)

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Plots for question 5.
A

B.

C. Answer: This one is correct - see previous page for identification of traces.


