ENGR4300 Fall 2005 Test 4A

| Name | | | |
|------|--|--|--|
| | | | |

| Section | | |
|---------|--|--|
| - | | |

Question 1 (25 points)_____

Question 2 (25 points)

Question 3 (25 points)_____

Question 4 (25 points)_____

Total (100 points): _____

Please do not write on the crib sheets.

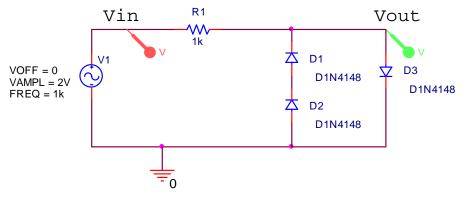
On all questions: SHOW ALL WORK. BEGIN WITH FORMULAS, THEN SUBSTITUTE VALUES <u>AND UNITS</u>. No credit will be given for numbers that appear without justification.

ENGR4300

Test 4A

Question 1 – Diodes (25 points)

Part A: In the following circuit, assume that Von for all of the diodes is 0.7V.



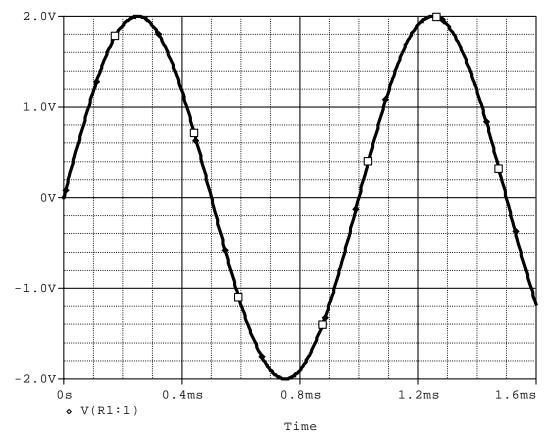
A1) Redraw what the circuit looks like for each of the three input voltages below. Replace the diodes that are on with voltage sources and the diodes that are off with open circuits. Indicate the voltage value of Vout. (3 pt each = 9 pt)

Vin = +2V:

Vin = -2V:

Vin = 0V:

ENGR4300

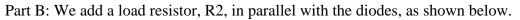


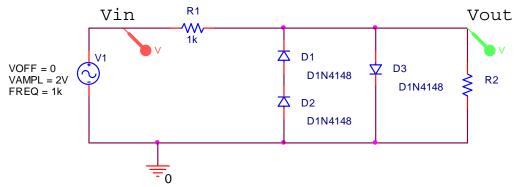
A2) Sketch the output at Vout on this graph of Vin. (4 pt)

A3) What are the maximum and minimum *currents* through resistor R1? (4 pt)

maximum current:

minimum current:

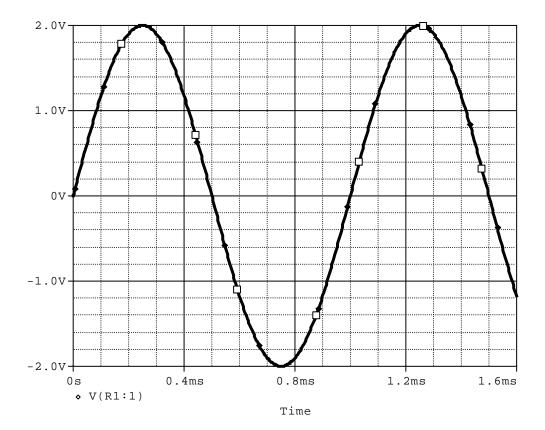




B1) If the load resistor is 1K Ω , what are the minimum and maximum *voltages* at Vout? (2 pt)

minimum voltage:

maximum voltage:



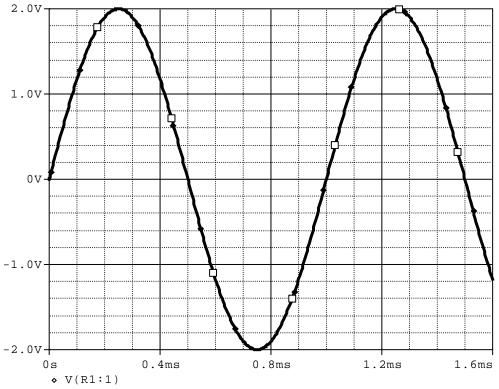
B2) Sketch Vout on the following graph of Vin for the load resistance of 1K Ω . (2 pt)

B3) If the load resistor is 200 Ω , what are the minimum and minimum *voltages* at Vout? (2 pt)

minimum voltage:

maximum voltage:

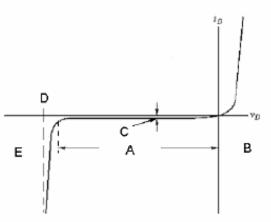
B4) Sketch Vout on the following graph of Vin for the load resistance of 200 Ω . (2 pt)



Time

Question 2 – Zener Diodes (25 points)

Part A: Zener Diode Characteristics



a) Identify the following as shown on the characteristic curve above, or indicate if it is not shown or non-existent (NA) (circle one) [4 points]:

| Zener Region | А | В | С | D | E | NA |
|---------------------------|---|---|---|---|---|----|
| Forward Bias Region | А | В | С | D | Е | NA |
| Asymptotic Current Region | А | В | С | D | Е | NA |
| Reverse Bias Region | А | В | С | D | Е | NA |
| Zener Voltage | А | В | С | D | Е | NA |
| Forward Voltage Limit | А | В | С | D | Е | NA |
| Operating Current | А | В | С | D | Е | NA |
| Saturation Current | А | В | С | D | E | NA |

b) You've worked with the 1N750 Zener Diode in the lab. What was its zener voltage? (circle one) [2 point]

0.7V 2.2V 4.7V 9V 12V 13.7V 24V 75V 100V

c) What was its forward voltage drop (Von)? (circle one) [2 point]

0.7V 2.2V 4.7V 9V 12V 13.7V 24V 75V 100V

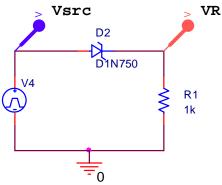
d) The junction in this zener diode and many other common diodes are made from the following: [1 point]

A. Face centered cubic and body centered cubic carbon film.

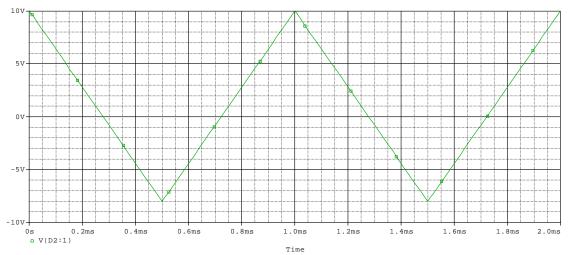
B. Tantalum diffusion bonded to tungsten.

- C. Silicon
- D. Rare earth super alloys

Part B: Zener Diode Circuit



The circuit shown above is excited by the following waveform at V4:



a) Determine Vsrc and VR for the plot shown above at the listed times. [6 points]

| Time | Vsrc | VR |
|-------|---------------|-------------|
| | [1/2 pt each] | [½ pt each] |
| Oms | | |
| 0.1ms | | |
| 0.2ms | | |
| 0.3ms | | |
| 0.4ms | | |
| 0.5ms | | |

b) Sketch the output of the circuit, VR, on the plot of the input shown. [4 points]

c) What is the (approximate) current flowing in the resistor at: [2 points each = 4 points]

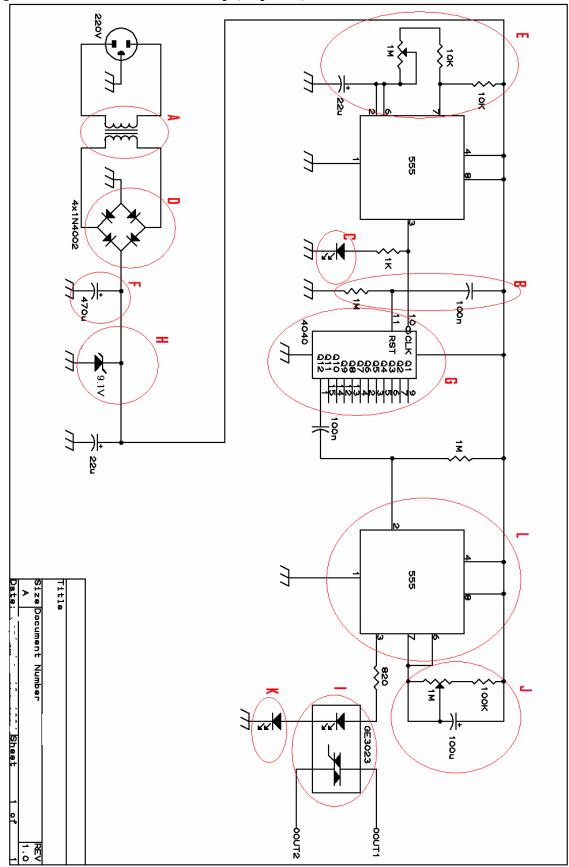
0.9ms:

1.3ms

А

d) Which of the following PSpice Simulation Settings would have been used have been used to create this graph? (Circle one) [2 point]

| <u>R</u> un to time: | 2ms | seconds (TSTOP) |
|--|-----------------------|---|
| Start saving data after: | .5ms | seconds |
| <u>T</u> ransient options | | |
| Maximum step size: | 5ms s | econds |
| Skip the initial tran | isient bias poi | nt calculation (SKIPBP) |
| В | | |
| <u>R</u> un to time: | 2ms | seconds (TSTOP) |
| Start saving data after: | 0 | seconds |
| _Iransient options | | |
| Maximum step size: | 01ms s | econds |
| Skip the initial trans | sient bias poi | nt calculation (SKIPBP) |
| С | | |
| <u>R</u> un to time: | 4ms | seconds (TSTOP) |
| Start saving data after: | 2ms | seconds |
| | | |
| <u>T</u> ransient options | | |
| <u>I</u> ransient options <u>M</u> aximum step size: | 01ms s | econds |
| <u>M</u> aximum step size: | | econds int calculation (SKIPBP) |
| <u>M</u> aximum step size: | | |
| <u>M</u> aximum step size: | | |
| Maximum step size: | | |
| Maximum step size: Skip the initial tran | nsient bias po | int calculation (SKIPBP) |
| Maximum step size: Skip the initial tran D <u>R</u> un to time: | nsient bias po 2ms | int calculation (SKIPBP) seconds (TSTOP) |
| Maximum step size: Skip the initial tran D <u>B</u> un to time: <u>S</u> tart saving data after: | 2ms | int calculation (SKIPBP) seconds (TSTOP) |



Question 3 – Circuit Functionality (25 points)

Given the schematic on the previous page, answer the questions that follow. [Hint: Although you have not used every component shown in the circuit, you should have no difficulty inferring functionality based on what you have learned in EI.]

a) What labeled (e.g. A-K) components are part of the power supply sub-circuit? (2 pts)

b) What is the source voltage of the circuit? Indicate (AC or DC) and (amplitude or voltage). (2 pts)

c) Which device in the circuit uses electromagnetism to provide electrical isolation between different parts of the circuit *and* how is this isolation achieved? (3 pts)

d) Does this power supply use a half-wave or full-wave rectifier? (1 pt)

e) What best describes the function of the Zener diode? (circle one) (1 pt)

| A) Transistor | B) H-Bridge | C) Battery |
|---------------|-------------|------------|
|---------------|-------------|------------|

D) Transformer E) Voltage Regulator F) FM Modulator

ENGR4300

f) Which 555 timer is configured in astable mode? (1 pt)

A) The one on the left side of the printB) The one on the right side of the printD) Both

g) Find the on-time of the multivibrator circuit containing the components labeled E, when the resistance of the 1Meg ohm variable resistor 10k ohms. (2 pts)

h) Assume the 555 timer is powered with 9VDC, how much current is likely flowing in the LED labeled C? Assume that Von for the LED is 2.1 volts. Show all work. (3 pts)

i) Pin 2 of the 555 timer is a: [Hint: Recall where pin 2 (trigger) is connected inside the 555-timer.] (1 pt)

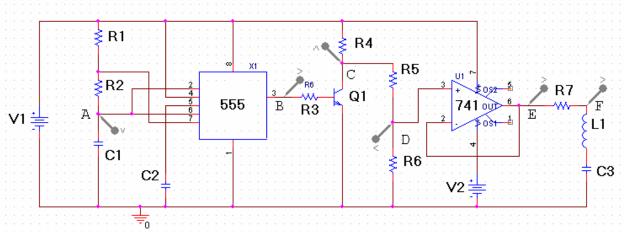
| A) Low-impedance input | B) High-impedance input |
|-------------------------|--------------------------|
| C) Low-impedance output | D) High-impedance output |

j) Assume that when power is first applied to the circuit, all capacitors are discharged. Explain what the R-C circuit labeled B does and how it accomplishes this. [Hint: What is the equation for the behavior of a capacitor? What happens when the circuit is first given voltage? ...once the desired voltage is achieved?] (3 pts)

k) Assuming that the outputs on the counter (labeled G) are ordered in the same way that the outputs on the 393 counter we used in experiment 7 are, how many pulses has the counter counted when it sends a pulse to the 555 timer labeled L? (3 pts)

1) If you decided to build this circuit and found a transformer with a turns ratio of 22:1, assuming other transformer parameters are suitable, would this work to provide our 555 timers with about 9VDC? Justify your answer. Show all work. (3 pts)

Question 4 – Ringing Pulse Circuit (25 points)



The circuit above generates a ringing pulse. Assume the components have the following values:

 $\begin{array}{l} C1=0.1\mu F,\ C2=0.01\mu F,\ C3=0.068\mu F\\ R1=1K\ \Omega,\ R2=10K\ \Omega,\ R3=1K\Omega,\ R4=1K\Omega,\ R5=9K\ \Omega,\ R6=1K,\ R7=50\Omega\\ R_L\ (the\ internal\ resistance\ of\ the\ fluorescent\ bulb)\ varies\ as\ the\ lamp\ functions.\\ L1=10mH\\ V1=+12\ V,\ V2=-12V \end{array}$

1) Circle and identify the following circuit elements (5 pt)

a. A voltage dividerb. An astable multivibratorc. An RLC circuitd. A transistor circuite. An op-amp circuit

2) What kind of op-amp circuit is e? (1 pt)

3) Calculate the frequency of the astable multivibrator in Hertz. (2 pt)

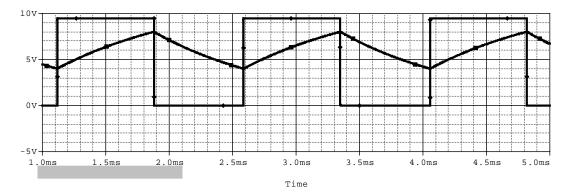
4) Fill in the voltages in the chart below based on the theoretical behavior of the circuit. In the row labeled LOW, give the voltages for all signals when the output at pin 3 of the 555 is low and in the row labeled HIGH, give the voltages for all signals when the output at pin 3 of the 555 is high. Assume all devices have no internal losses .(10 pt)

| Output at pin 3 | point A (voltage) | 1 | point C (voltage) | 1 | point E (voltage) |
|--------------------|----------------------|---|----------------------|---|----------------------|
| LOW | | | | | |
| HIGH | | | | | |

Calculations:

5) Calculate the resonant frequency in Hertz of the signal at F. (2 pt)

6) Identify which of the following plots goes with which block of the circuit (A-B, B-C, C-D, D-E, E-F) All graphs have two signals. (5 pt)



Test 4A

